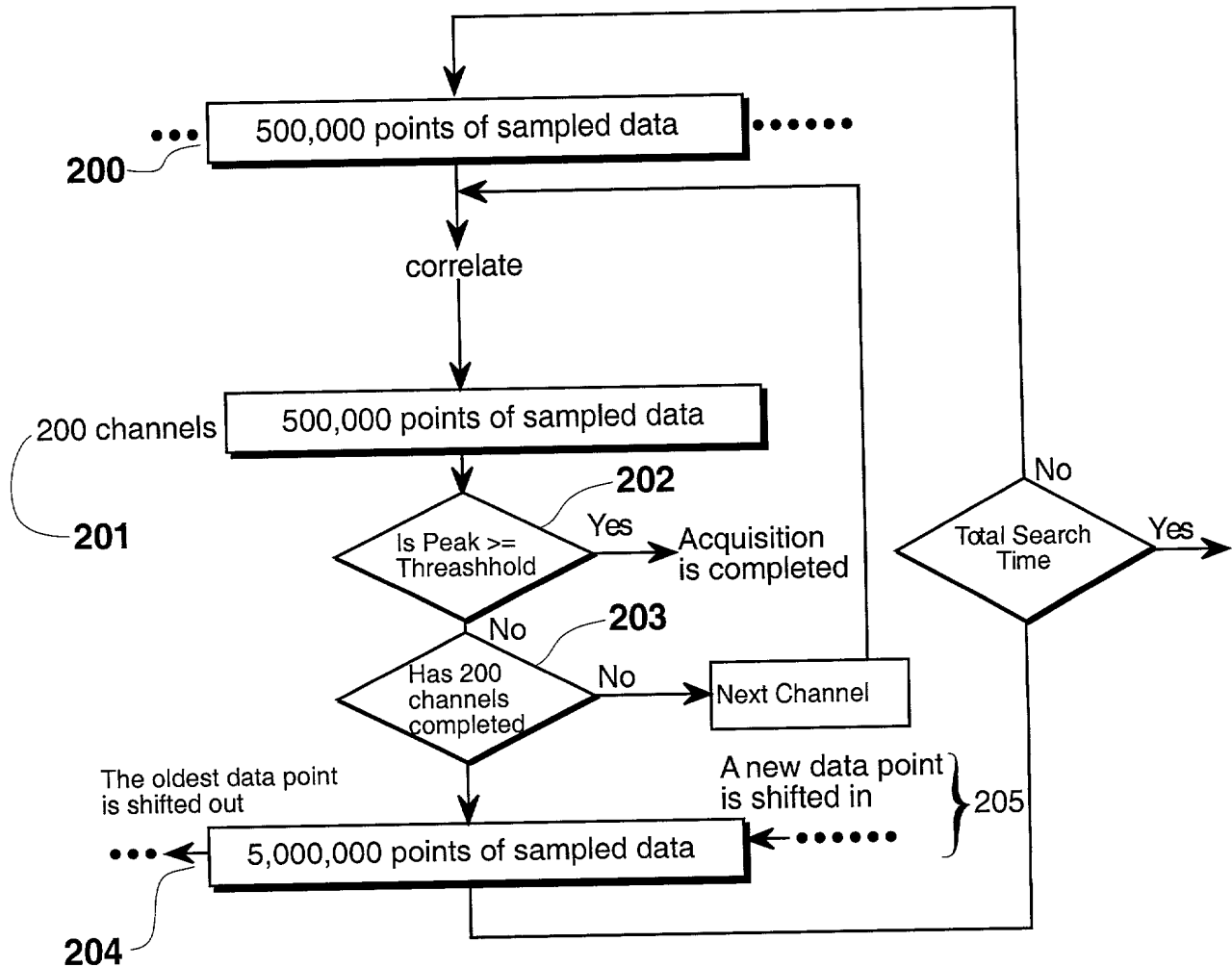
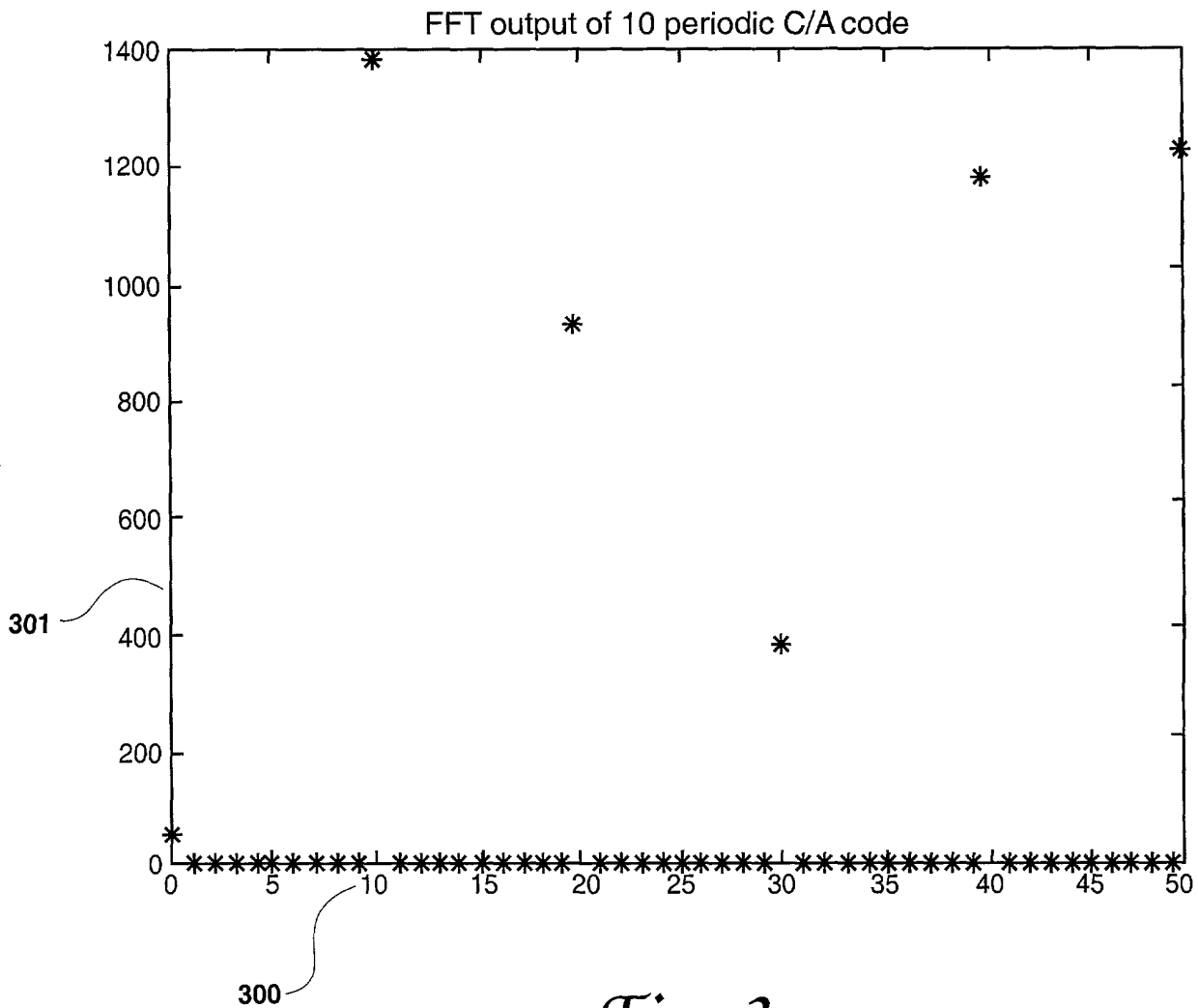


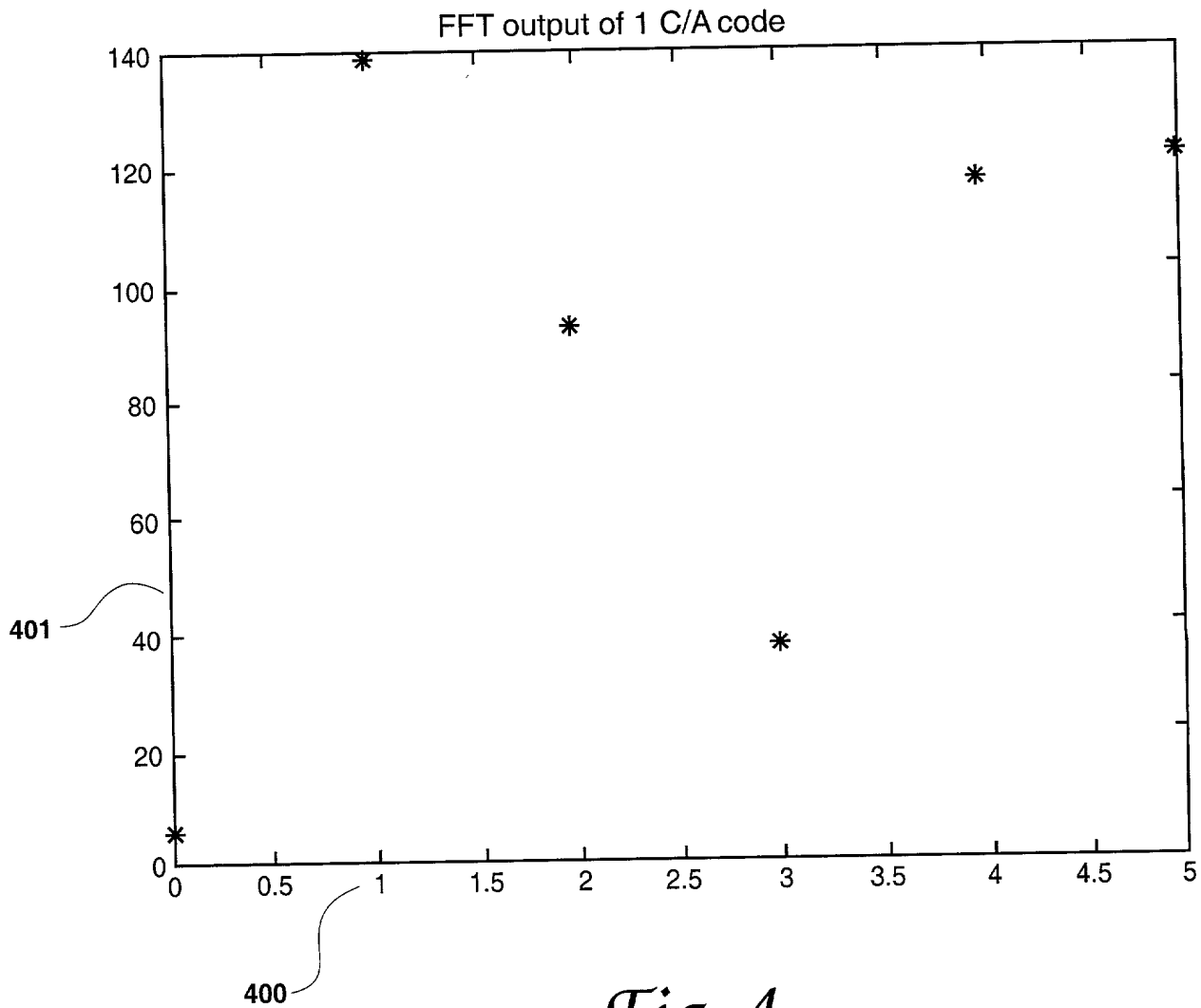
*Fig. 1*  
(PRIOR ART)



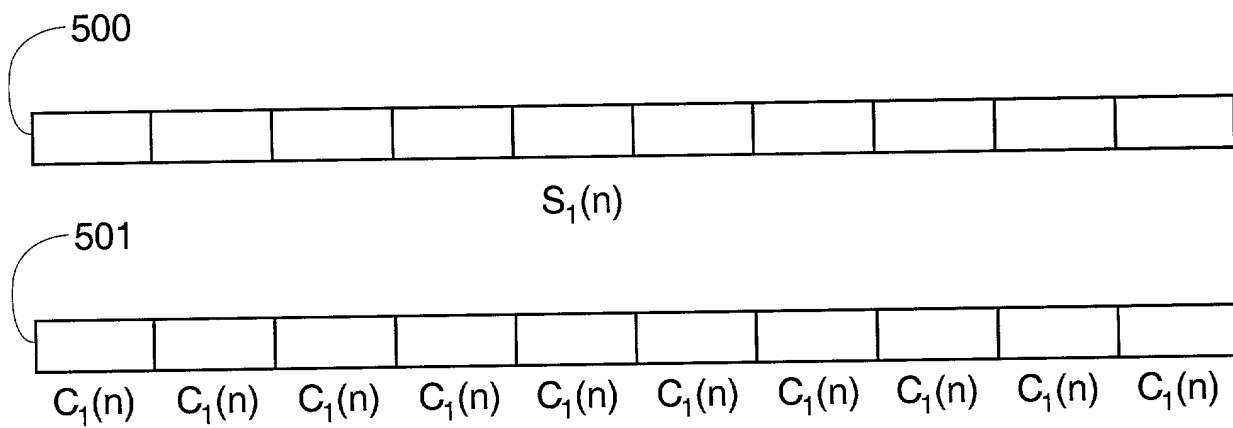
*Fig. 2*  
(PRIOR ART)



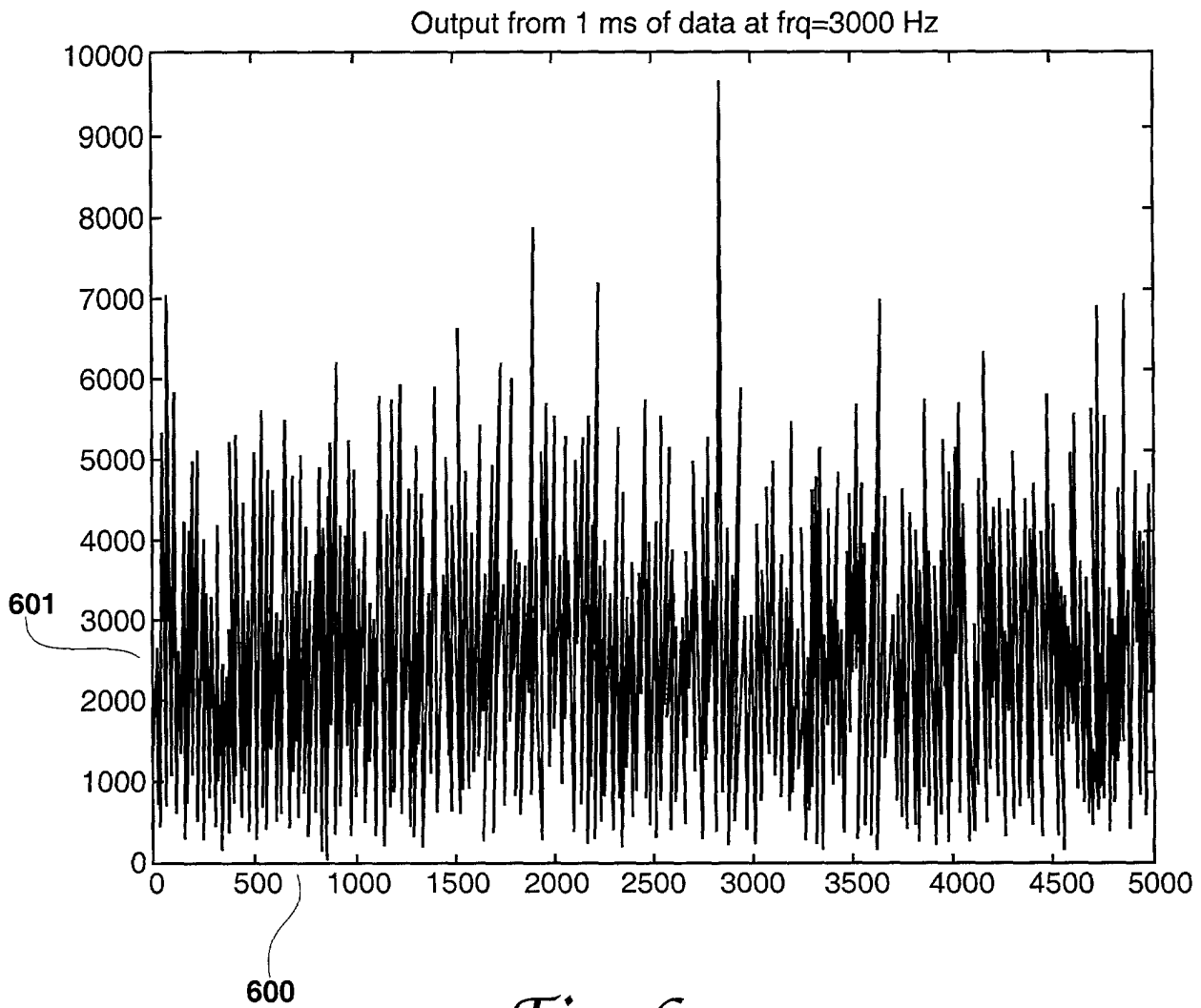
*Fig. 3*



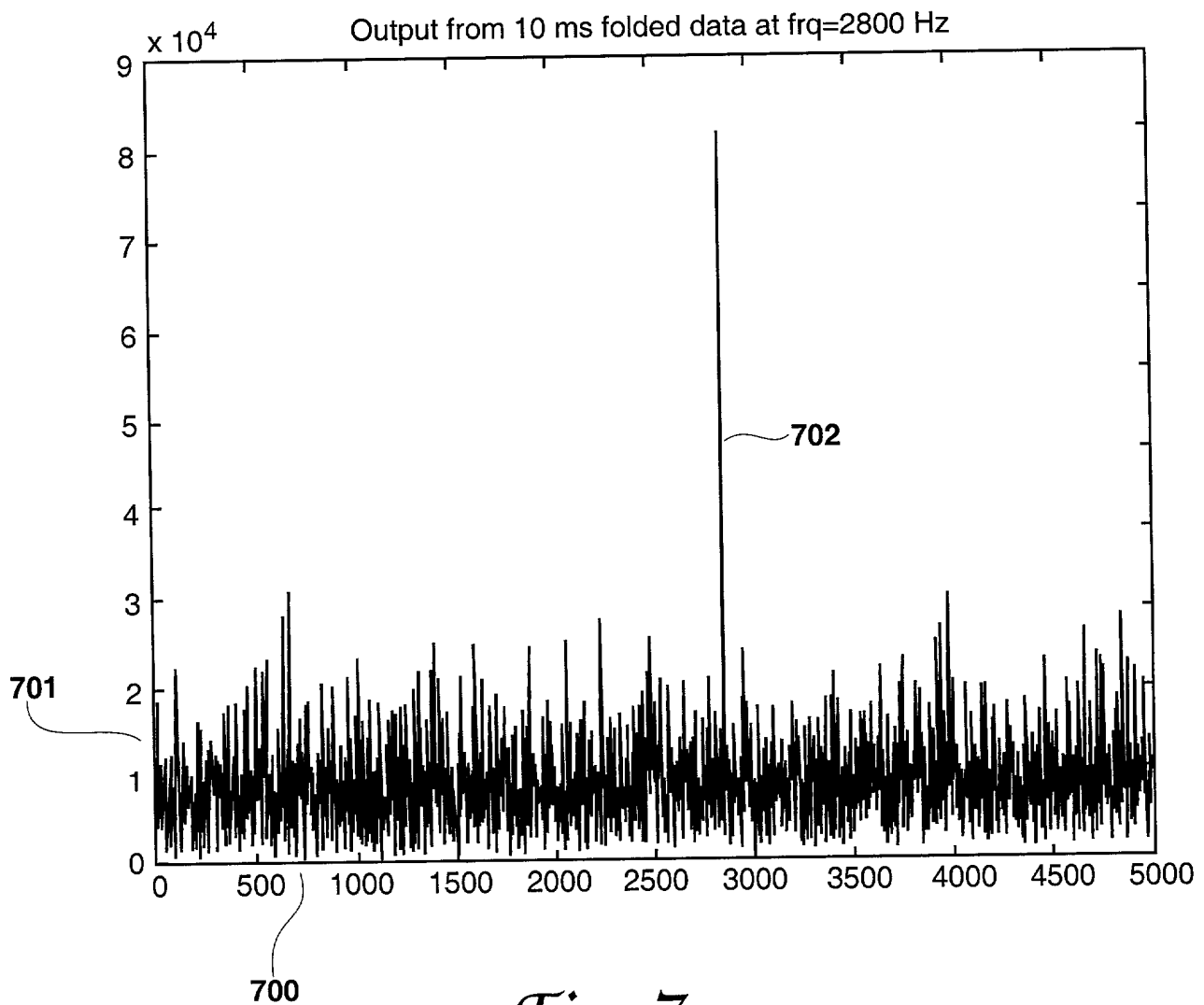
*Fig. 4*



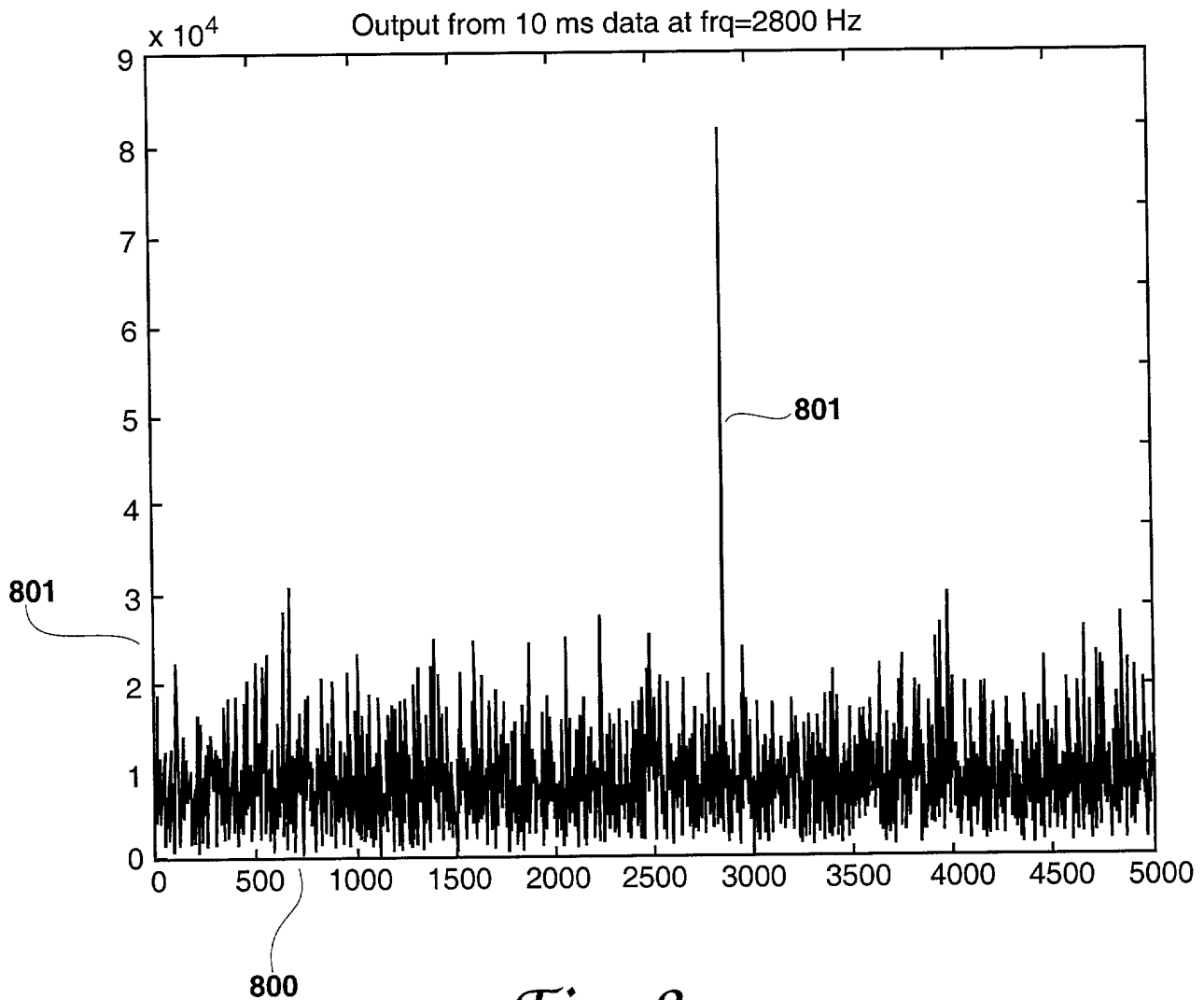
*Fig. 5* input signal and 10 cycles of C/A code



*Fig. 6*



*Fig. 7*



*Fig. 8*